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TRANSMITTAL		Application / Conf. No.	10/706,115 / 7705
		Filing Date	November 12, 2003
		First Named Inventor	Kevin T. Look
(to be used for all correspondence after initial filing)		Examiner Name	Steven J. Fulk
Mail Stop: AMENDMENT		Art Unit	2891
Express Mail Receipt No.		Patent No.	
Total Number of Pages in This Submission		Attorney Docket Number	X-1144 US
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IN THE UNITED STATES PATENT OFFICE

pplicant:

Kevin T. Look

Assignee:

Xilinx, Inc.

Title:

"Low Voltage Non-Volatile Memory Transistor"

Serial No.:

10/706,115

File Date:

11-12-2003

Examiner:

Steven J. Fulk

Art Unit:

2891

Docket No.: X-1144 US

Conf. No.:

7705

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Alexandria, Virginia 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

In response to the Restriction Requirement mailed from the Patent Office on June 21, 2005, Applicant elects with traverse to prosecute Invention I: Claims 1-17 and 27-30. Applicant hereby cancels Claims 18-26 (Invention II) but reserves the right to prosecute Invention II (Claims 18-26) in a subsequent divisional application.

A listing of the claims in the present application is appended hereto.

Respectfully submitted,

Justin Liu

Attorney for Applicant

Reg. No. 51,959

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on June 29, 2005.

Julie Matthews Name